

	L #	Hits	Search Text	DBs
1	L1	22	(translat\$3 conver\$4 emulat\$3) near20 ("16" adj1 bit near2 instruction) near20 ("32" adj1 bit near2 instruction)	US-PGPUB; USPAT
2	L2	3	(translat\$3 conver\$4 emulat\$3) near20 ("8" adj1 bit near2 instruction) near20 ("16" adj1 bit near2 instruction)	US-PGPUB; USPAT
3	L3	10	(translat\$3 conver\$4 emulat\$3) near20 ("32" adj1 bit near2 instruction) near20 ("64" adj1 bit near2 instruction)	US-PGPUB; USPAT
4	L4	0	(translat\$3 conver\$4 emulat\$3) near20 ("8" adj1 bit near2 instruction) near20 ("16" adj1 bit near2 instruction)	USOCR; EPO; JPO; DERWENT; IBM_TDB
5	L5	6	(translat\$3 conver\$4 emulat\$3) near10 ("16" adj1 bit adj2 instruction) near10 ("32" adj1 bit adj1 instruction)	USOCR; EPO; JPO; DERWENT; IBM_TDB
6	L6	1	(translat\$3 conver\$4 emulat\$3) near10 ("32" adj1 bit adj2 instruction) near10 ("64" adj1 bit adj1 instruction)	USOCR; EPO; JPO; DERWENT; IBM_TDB
7	L7	1	(translat\$3 conver\$4 emulat\$3) near20 (((("4" four) adj1 byte) word doubleword) near2 instruction) near20 (((("8" eight) adj1 byte) doubleword quad) near2 instruction)	USOCR; EPO; JPO; DERWENT; IBM_TDB
8	L8	1	(translat\$3 conver\$4 emulat\$3) near20 (((("2" two) adj1 byte) word) near2 instruction) near20 (((("4" four) adj1 byte) doubleword) near2 instruction)	USOCR; EPO; JPO; DERWENT; IBM_TDB
9	L9	2	(translat\$3 conver\$4 emulat\$3) near20 (("1" one single) adj1 byte near2 instruction) near20 (((("2" two) adj1 byte) word) near2 instruction)	US-PGPUB; USPAT
10	L10	2	(translat\$3 conver\$4 emulat\$3) near20 (("1" one single) adj1 byte near2 instruction) near20 (((("2" two) adj1 byte) word) near2 instruction)	US-PGPUB; USPAT
11	L11	1	(translat\$3 conver\$4 emulat\$3) near20 (((("2" two) adj1 byte) word) near2 instruction) near20 (((("4" four) adj1 byte) doubleword) near2 instruction)	US-PGPUB; USPAT
12	L12	2	(translat\$3 conver\$4 emulat\$3) near20 (((("4" four) adj1 byte) word doubleword) near2 instruction) near20 (((("8" eight) adj1 byte) doubleword quad) near2 instruction)	US-PGPUB; USPAT
13	L13	194	instruction near20 ((low lower) adj2 register)	US-PGPUB; USPAT
14	L14	230	instruction near20 ((high upper higher) adj2 register)	US-PGPUB; USPAT
15	L15	59	L13 and L14	US-PGPUB; USPAT
16	L16	26	instruction near20 ((high upper higher) adj1 (portion half) adj2 register)	US-PGPUB; USPAT
17	L17	45	instruction near20 ((low lower) adj1 (portion half) adj2 register)	US-PGPUB; USPAT
18	L18	21	L16 and L17	US-PGPUB; USPAT
19	L19	56	(translat\$3 conver\$4 expan\$4) near10 ("16" adj1 bit adj2 (operand data)) near10 ("32" adj1 bit adj2 (operand data))	US-PGPUB; USPAT
20	L20	48	(translat\$3 conver\$4 expan\$4) near10 ("32" adj1 bit adj2 (operand data)) near10 ("64" adj1 bit adj2 (operand data))	US-PGPUB; USPAT